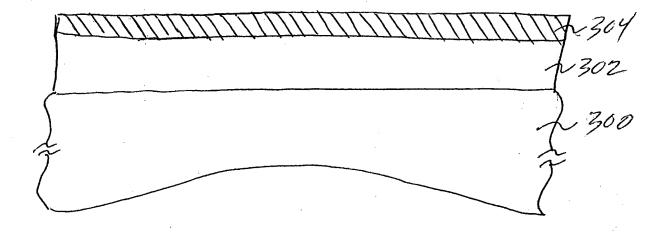
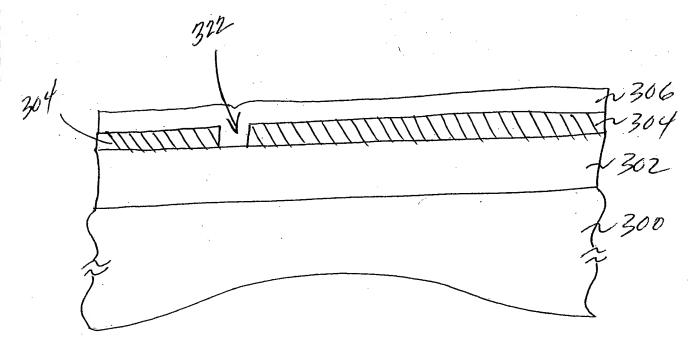
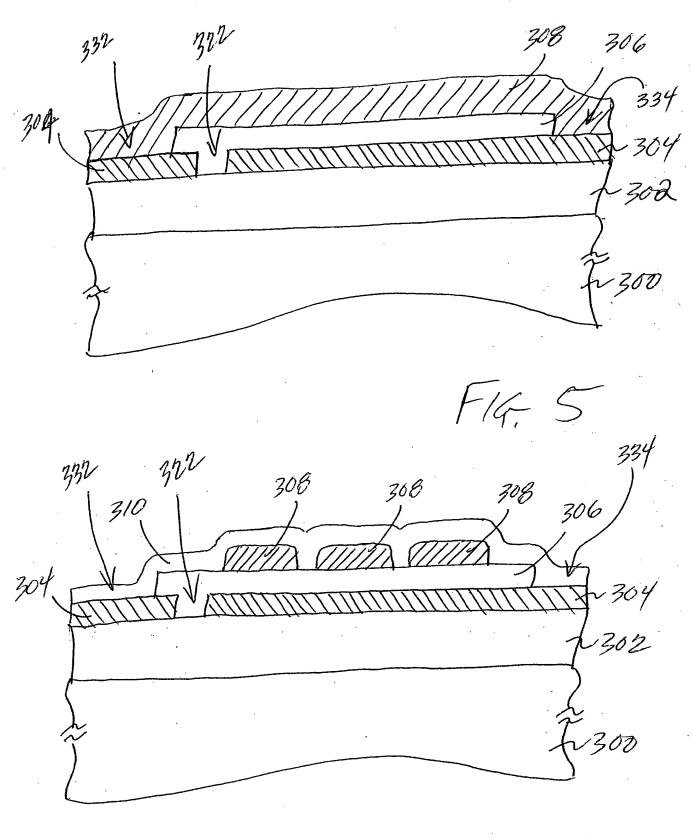


FORM FIRST DIELECTRIC LAYER OVER SEMICONDUCTOR SUBSTRATE 222
√
FORM FIRST MAGNETIC LAYER OVER FIRST DIELECTRIC LAYER
PATTERN FIRST MAGNETIC LAYER TO DEFINE AT LEAST ONE SLOT
V
FORM SECOND DIELECTRIC LAYER OVER FIRST MAGNETIC LAYER, FILLING EACH SLOT IN FIRST MAGNETIC LAYER
PATTERN SECOND DIELECTRIC LAYER TO DEFINE AT LEAST ONE VIA TO FIRST MAGNETIC LAYER
FORM CONDUCTIVE LAYER OVER SECOND DIELECTRIC LAYER 212
V
PATTERN CONDUCTIVE LAYER TO FORM A CONDUCTOR HAVING A SPIRAL-SHAPED SIGNAL PATH AND TO CLEAR ANY VIAS TO FIRST MAGNETIC LAYER
V
FORM THIRD DIELECTRIC LAYER OVER CONDUCTIVE LAYER W 216
PATTERN THIRD DIELECTRIC LAYER TO DEFINE AT LEAST ONE VIA TO FIRST MAGNETIC LAYER
FORM SECOND MAGNETIC LAYER OVER THIRD DIELECTRIC LAYER, FILLING ANY VIAS TO FIRST MAGNETIC LAYER
PATTERN SECOND MAGNETIC LAYER TO FORM AT LEAST ONE SLOT



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FORM MAGNETIC MATERIAL LAYER OVER UNDERLYING LAYER

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FORM OVERLYING LAYER OVER MAGNETIC MATERIAL LAYER

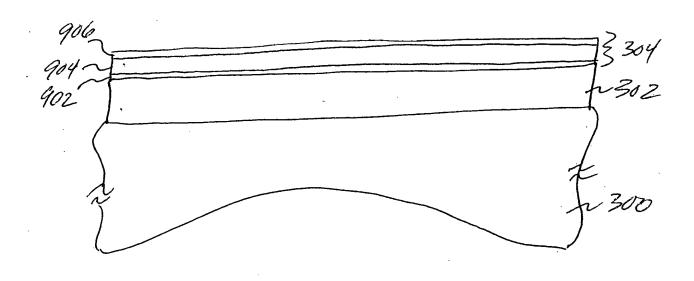
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FORM PATTERNED MASK LAYER OVER OVERLYING LAYER

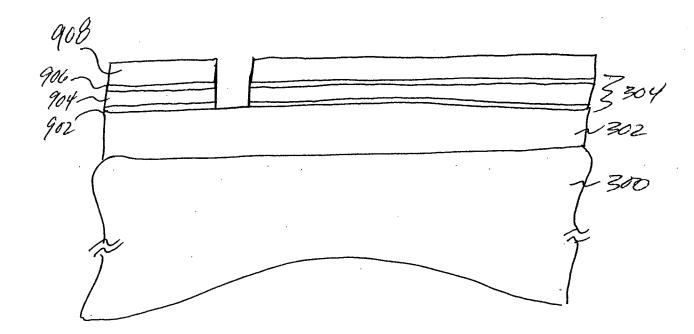
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ETCH UNDERLYING, MAGNETIC MATERIAL, AND OVERLYING LAYERS

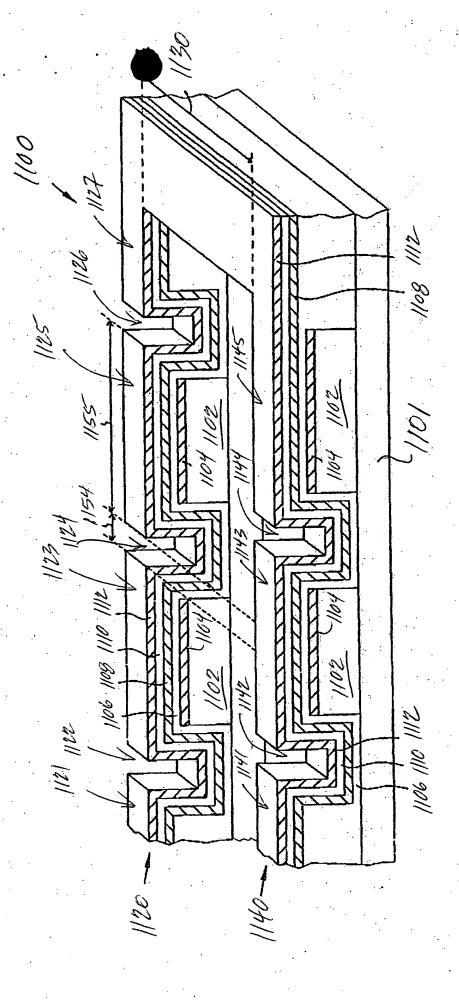
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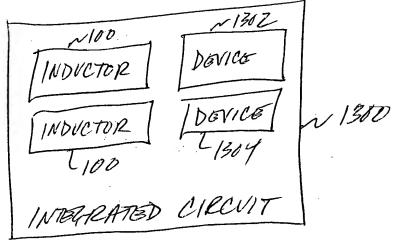


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1/4/

1700 FOI	RM FIRST DIELECTRIC LAYER OVER SEMICONDUCTOR SUBSTRATE V/202
10	\bigvee
	FORM FIRST MAGNETIC LAYER OVER FIRST DIELECTRIC LAYER 1/204
7 -	V
P	ATTERN FIRST MAGNETIC LAYER AND FIRST DIELECTRIC LAYER TO DEFINE PEDESTALS AND TRENCHES
L	
F	ORM SECOND DIELECTRIC LAYER OVER FIRST MAGNETIC LAYER AND IN TRENCHES
L	
	PATTERN SECOND DIELECTRIC LAYER V/2/0
]	FORM CONDUCTIVE LAYER OVER SECOND DIELECTRIC LAYER W/2/2
	PATTERN CONDUCTIVE LAYER 12/4
*	
	FORM THIRD DIELECTRIC LAYER OVER CONDUCTIVE LAYER 1/2/6
F L	1 1000
en e	PATTERN THIRD DIELECTRIC LAYER 1216
<u>.</u>	
FC	ORM SECOND MAGNETIC LAYER OVER THIRD DIELECTRIC LAYER
<u> </u>	
	PATTERN SECOND MAGNETIC LAYER 1222



INTEGRATED NIGOTOR INDUCTOR LIPO LIPO LIPO LIPO LIPO LIPO